



PTO/SB/08A (08-03)

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**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

Sheet 1

of 2

**Complete if Known**

Application Number	10/801,618
Filing Date	March 16, 2004
First Named Inventor	Po Ming Li
Art Unit	2817
Examiner Name	P. T. Nguyen
Attorney Docket Number	2085-01800

**U. S. PATENT DOCUMENTS**

Examiner Initials*	Cite No. <sup>1</sup>	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code <sup>2</sup> (if known)			
Duplicate	AA	US- 6,370,372	04-09-2002	Molnar et al.	
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**FOREIGN PATENT DOCUMENTS**

Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages Or Relevant Figures Appear	T <sup>6</sup>
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Substitute for form 1449/PTO  <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (Use as many sheets as necessary)		<b>Complete if Known</b>	
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Examiner Name	P. T. Nguyen		
Sheet 2	of 2	Attorney Docket Number	2085-01800

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
PN	AB	J. CHOMA, "A Three-Level Broad-Banded Monolithic Analog Multiplier", IEEE J. Solid-State Circuits, Vol. SC-16, No. 4, August 1981, pp. 392-399.	
PN	AC	B. GILBERT, "Design Considerations For BJT Active Mixers", Low-Power HF Microelectronics: A Unified Approach, IEE., 1996, pp. 837-927.	
PN	AD	D. Y. KIM, S.G. LEE, J. H. LEE, "Up-Conversion Mixer For PCS Application Using SiBJT", IEEE 2000 2nd International Conference on Microwave and Millimetre Wave Technology Proceedings, pp. 424-427.	
PN	AE	M. KOYAMA, T. ARAI, H. TANIMOTO, Y. YOSHIDA, "A 2.5-V Active Low-Pass Filter Using All N-P-N Gilbert Cells With A 1 V P-P Linear Input Range", IEEE J. Solid-State Circuits, Vol. 28, pp. 1246-53, December 1993, pp. 1246-1253.	

Examiner Signature	Patricia Nguyen	Date Considered	12/20/05
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